ARC 2024

20th International Symposium on Applied Reconfigurable Computing

Aveiro, Portugal | March 20-22, 2024
University of Aveiro

CALL FOR PAPERS

IMPORTANT DATES

December 15, 2023
Paper Submission

January 10, 2024
Decision Notification

January 30, 2024
Author Registration

February 7, 2024
Camera-Ready Paper Submission

SYMPOSIUM COMMITTEES

General Chair
Pedro C. Diniz
University of Porto, Portugal

Program Chairs
Iouliia Skliarova
University of Aveiro, Portugal
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Microelectronics Institute of Seville, Spain

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Applied Reconfigurable Computing focuses on the use of reconfigurable hardware, such as field-programmable gate arrays (FPGAs), to accelerate and optimize various computational tasks and applications. It involves designing and implementing hardware configurations that can be dynamically adapted to specific workloads, improving performance and efficiency in a wide range of applications. The 20th edition of the symposium aims to bring together researchers and practitioners of reconfigurable computing with an emphasis on practical applications of this promising technology.

The ARC'2024 proceedings will be published as a volume in Springer's Lecture Notes in Computer Science (LNCS) series and will also be available through the SpringerLink online service.

TOPICS OF INTEREST

Papers in English in all areas of applied reconfigurable computing are invited, with particular emphasis on:

» Design Methods & Tools
High-level languages & compilation
Simulation & synthesis
Design space exploration

» Applications
Security & cryptography
Embedded computing & DSP
Robotics, space, bioinformatics
Deep learning & neural networks

» Architectures
Computation in/near memory
Self-adaptive, evolvable
PSoCs & adaptive SoCs
Low-power designs
Approximate computing
Fine-/coarse-/mixed-grained
Interconnect (NoCs, ...)
Resilient & fault tolerant

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Lecture Notes in Computer Science
LNCS   LNAI   LNBI
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